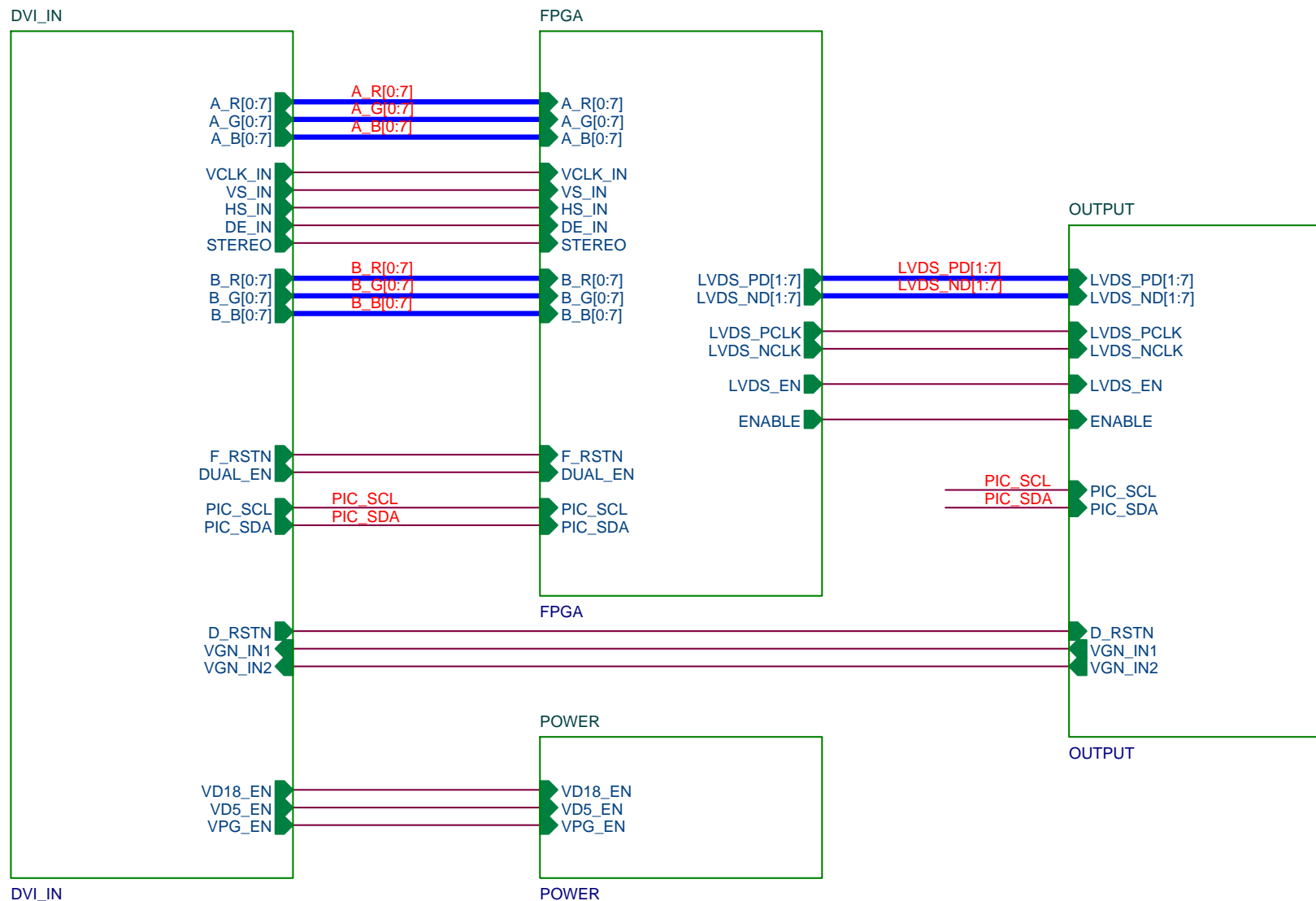


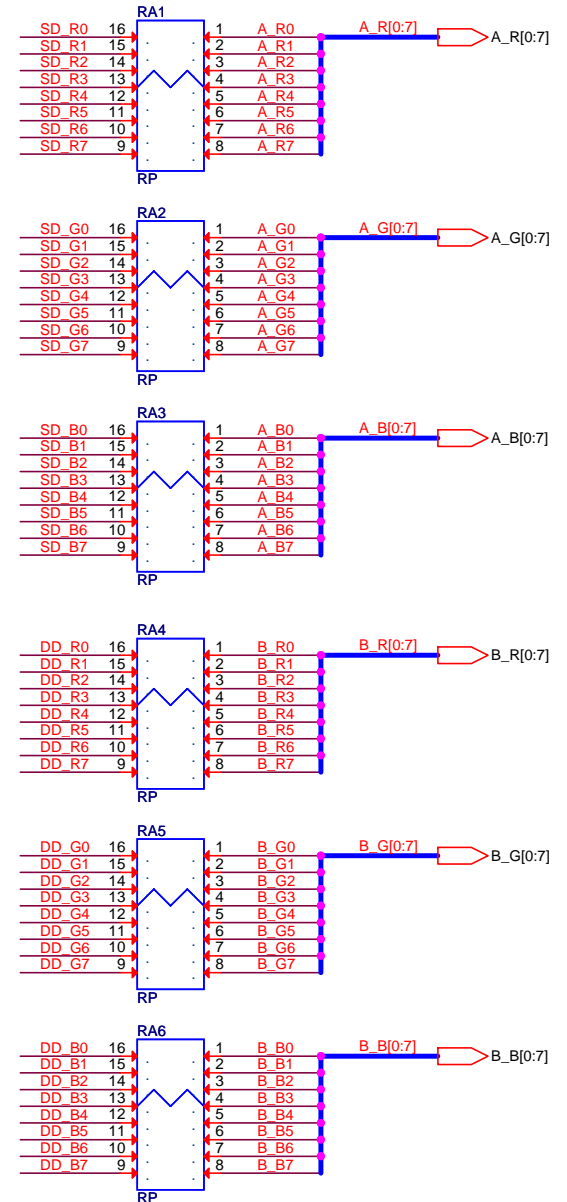
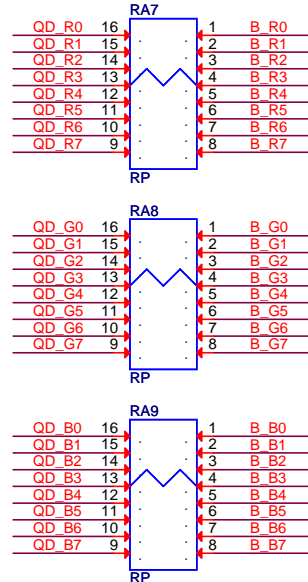
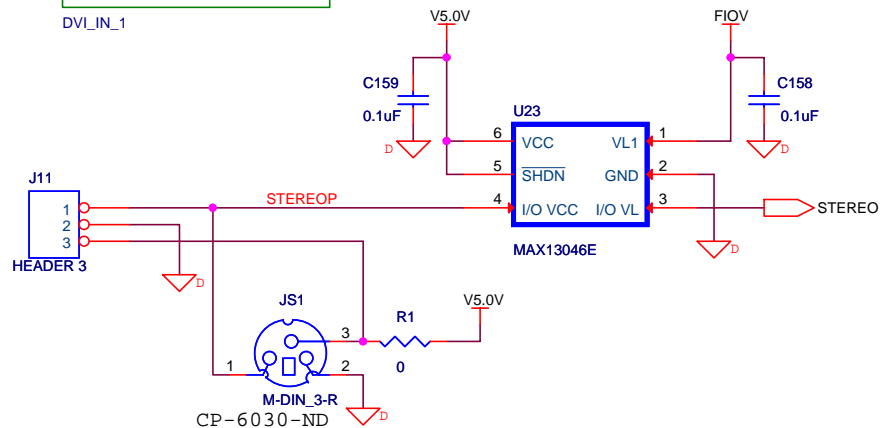
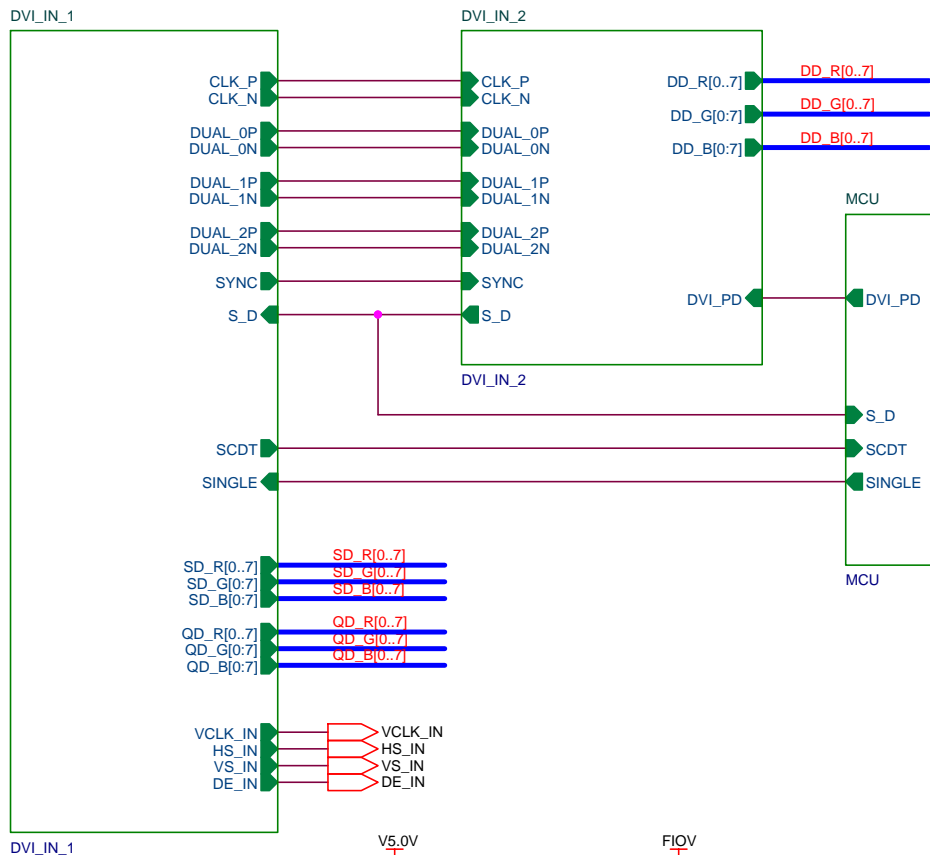
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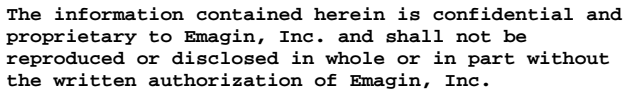
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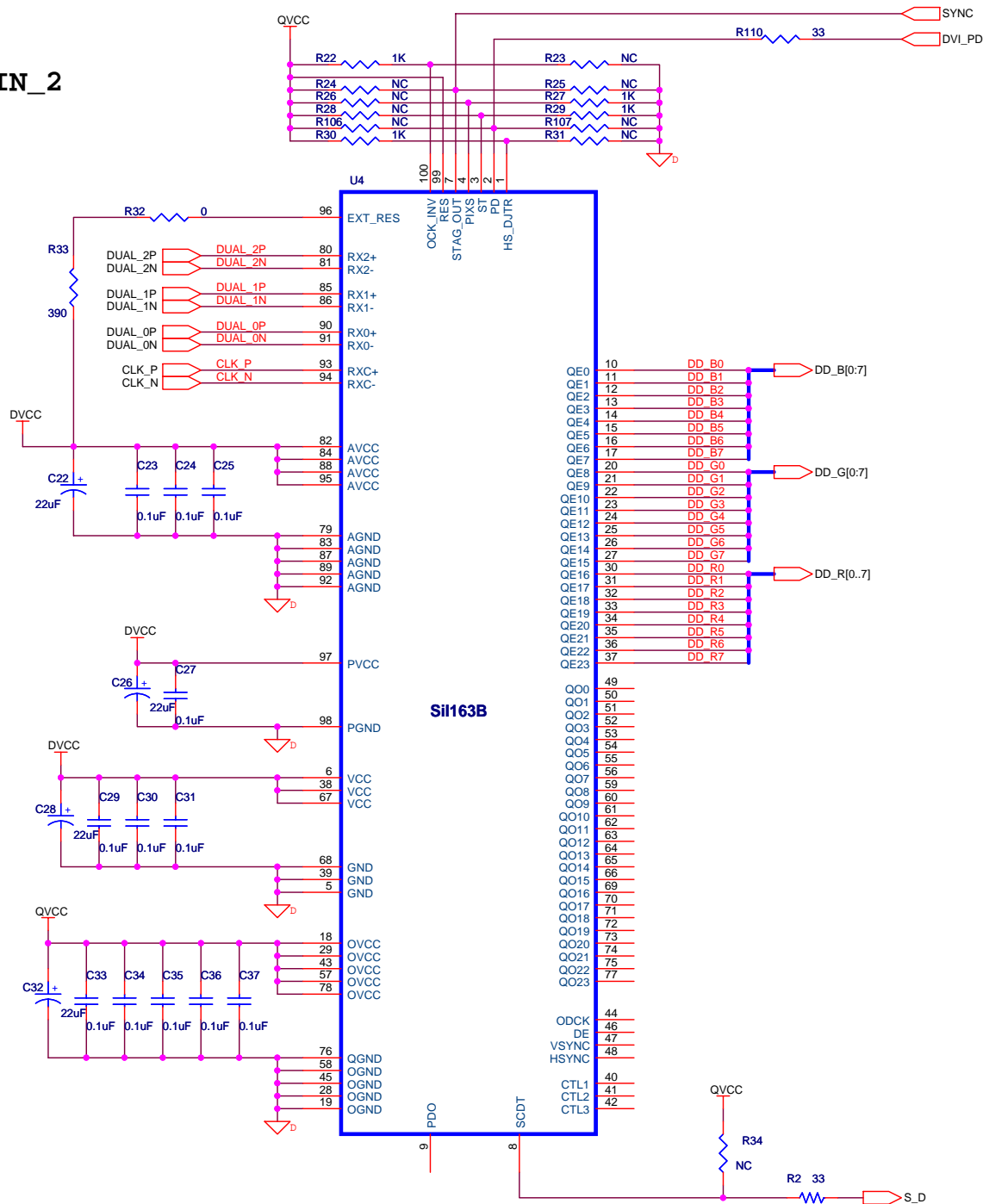


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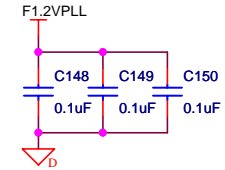
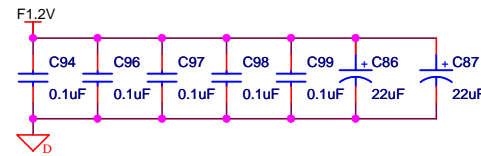
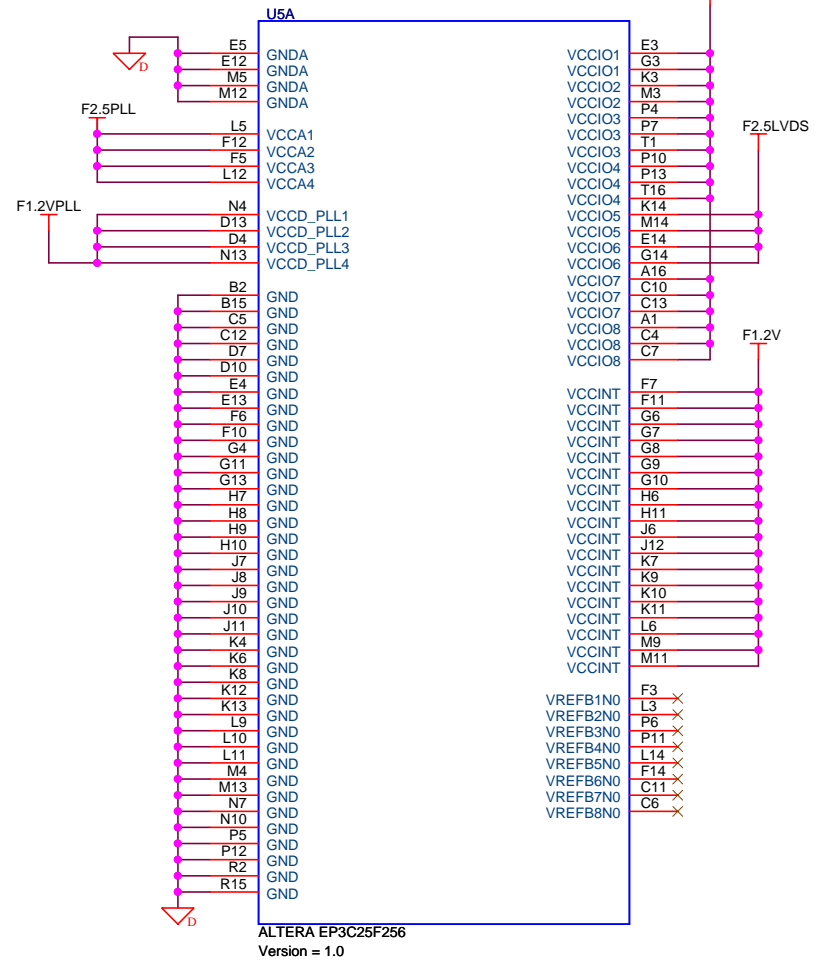
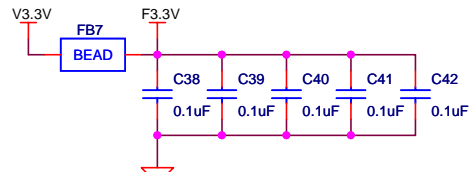
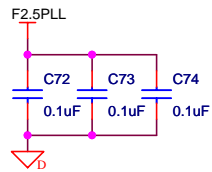
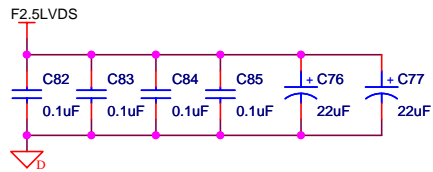
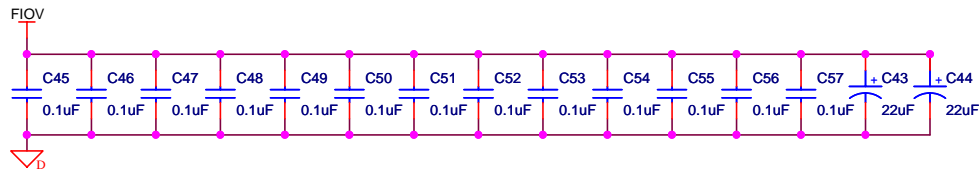
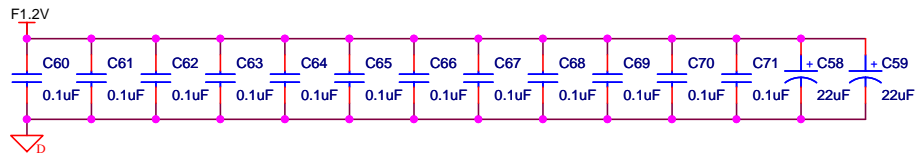
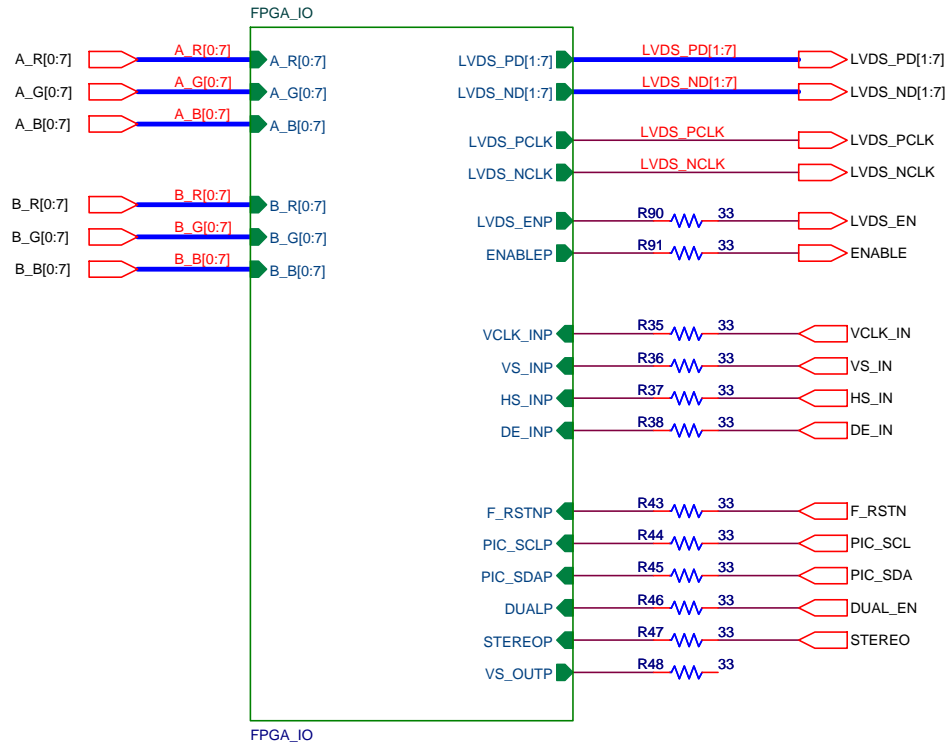
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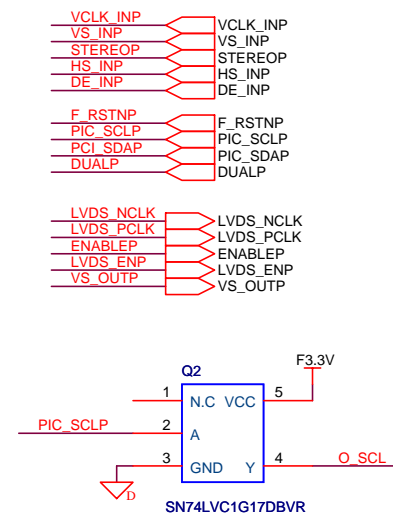
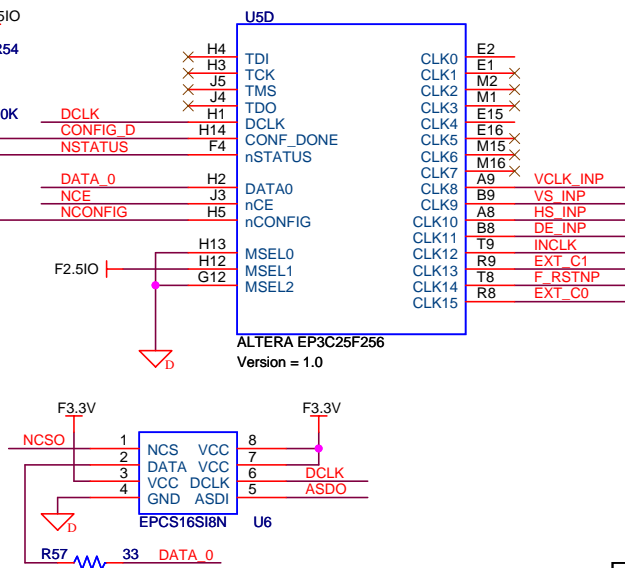
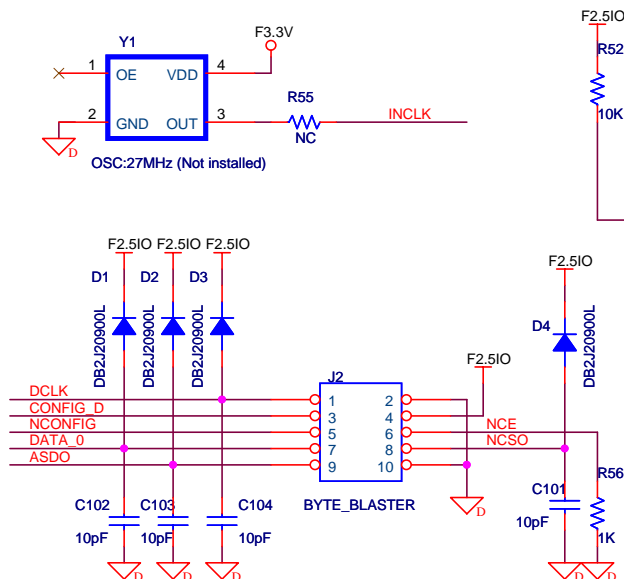
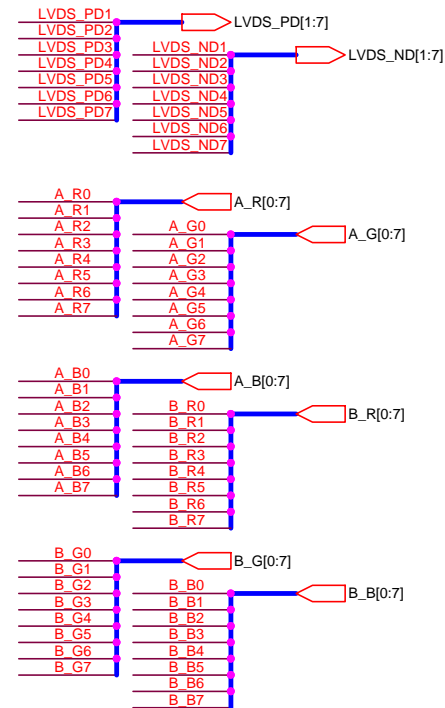
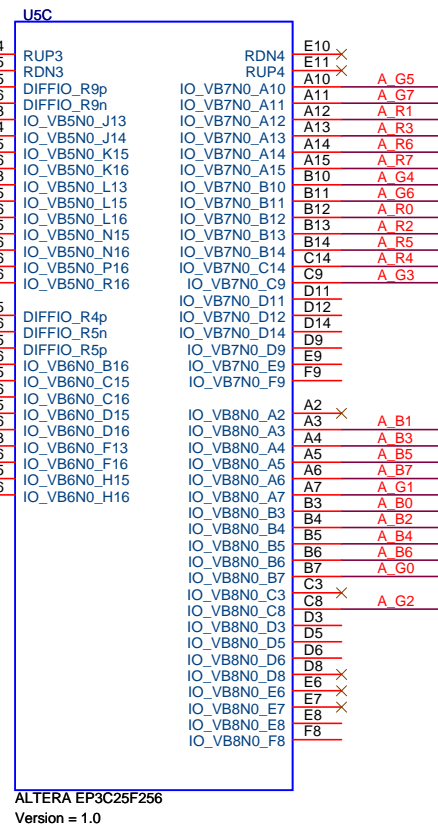
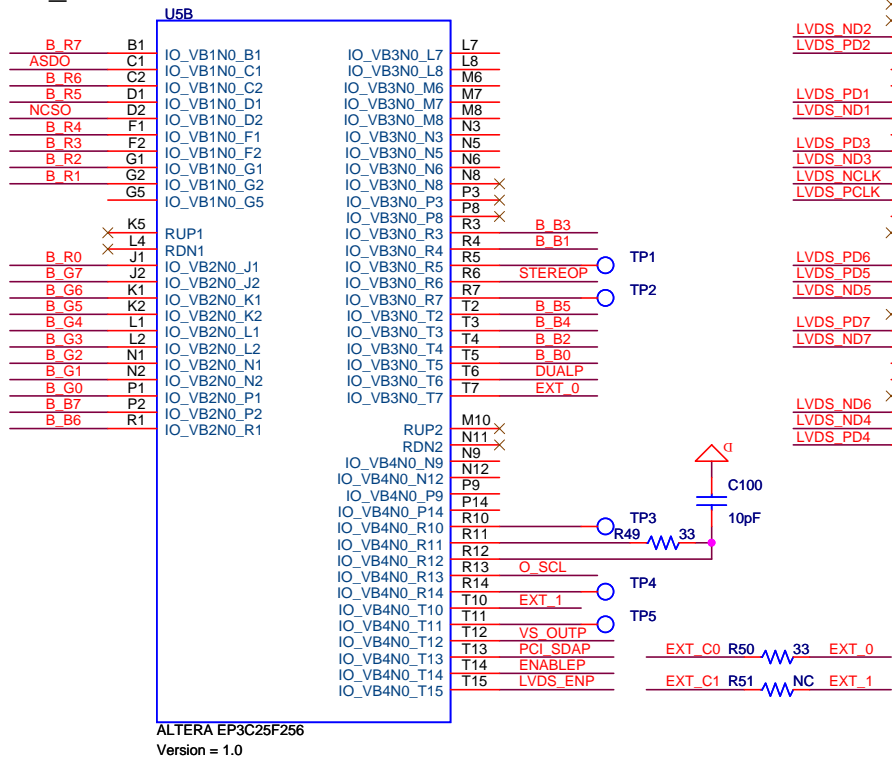
FPGA



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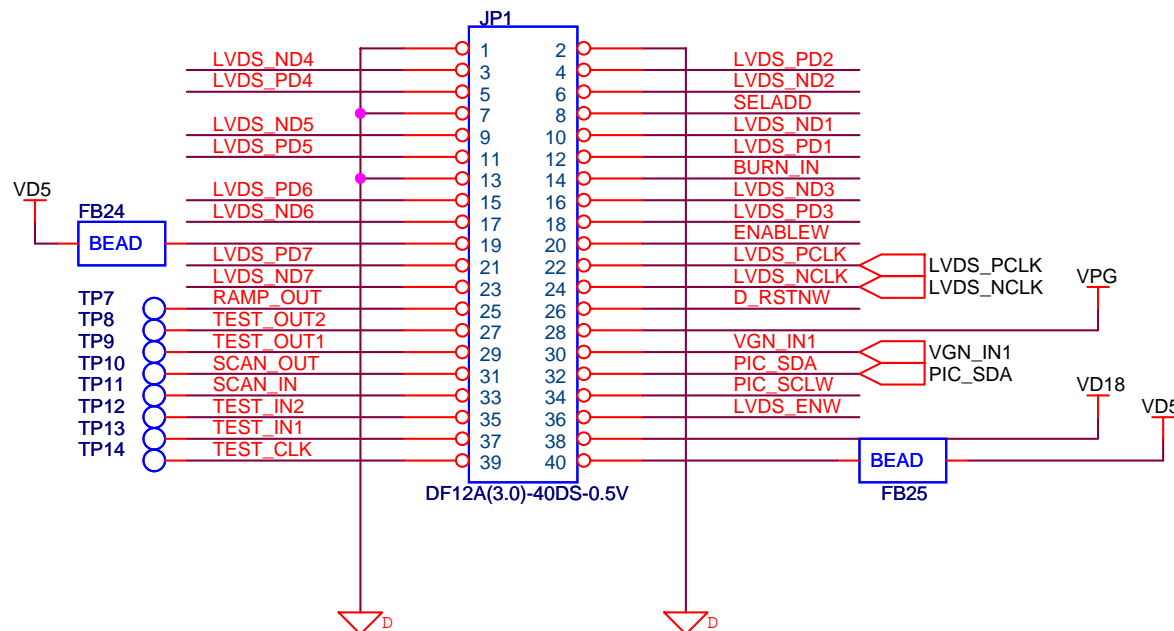
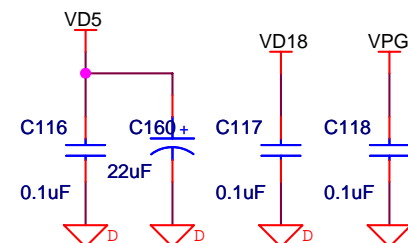
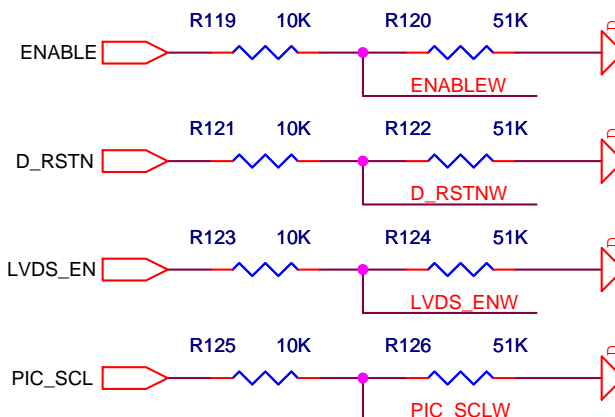
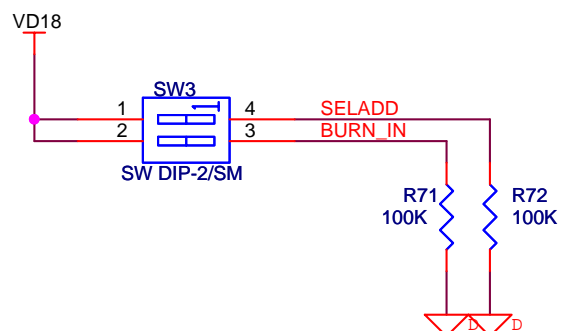
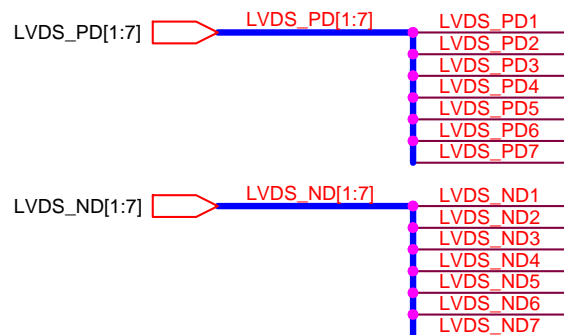
FPGA_IO



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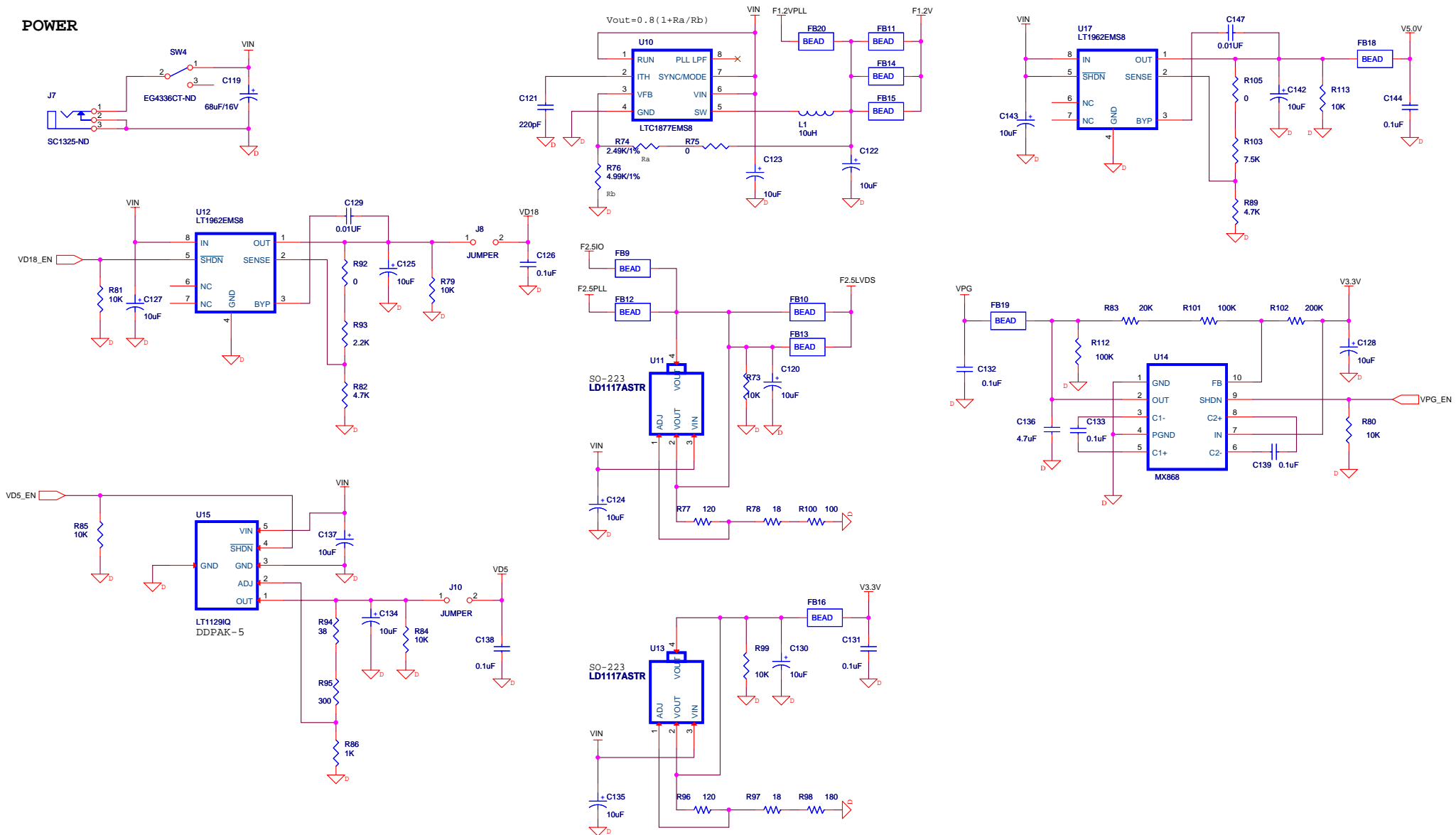
OUTPUT



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POWER



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