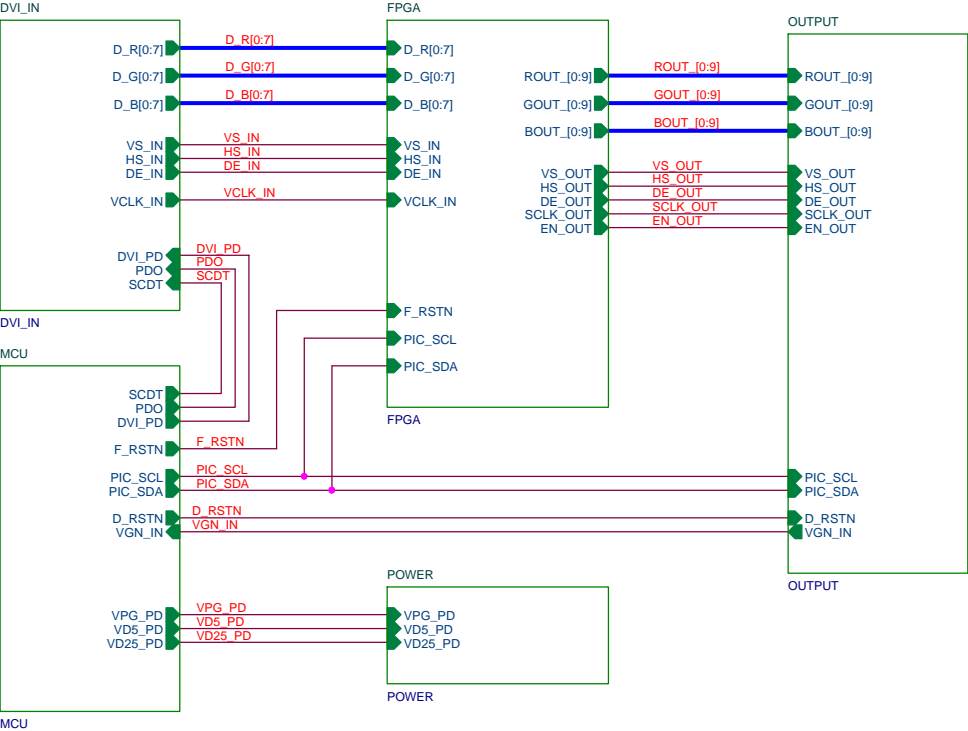



EMAGIN\_SXGA\_FPGA\_V3



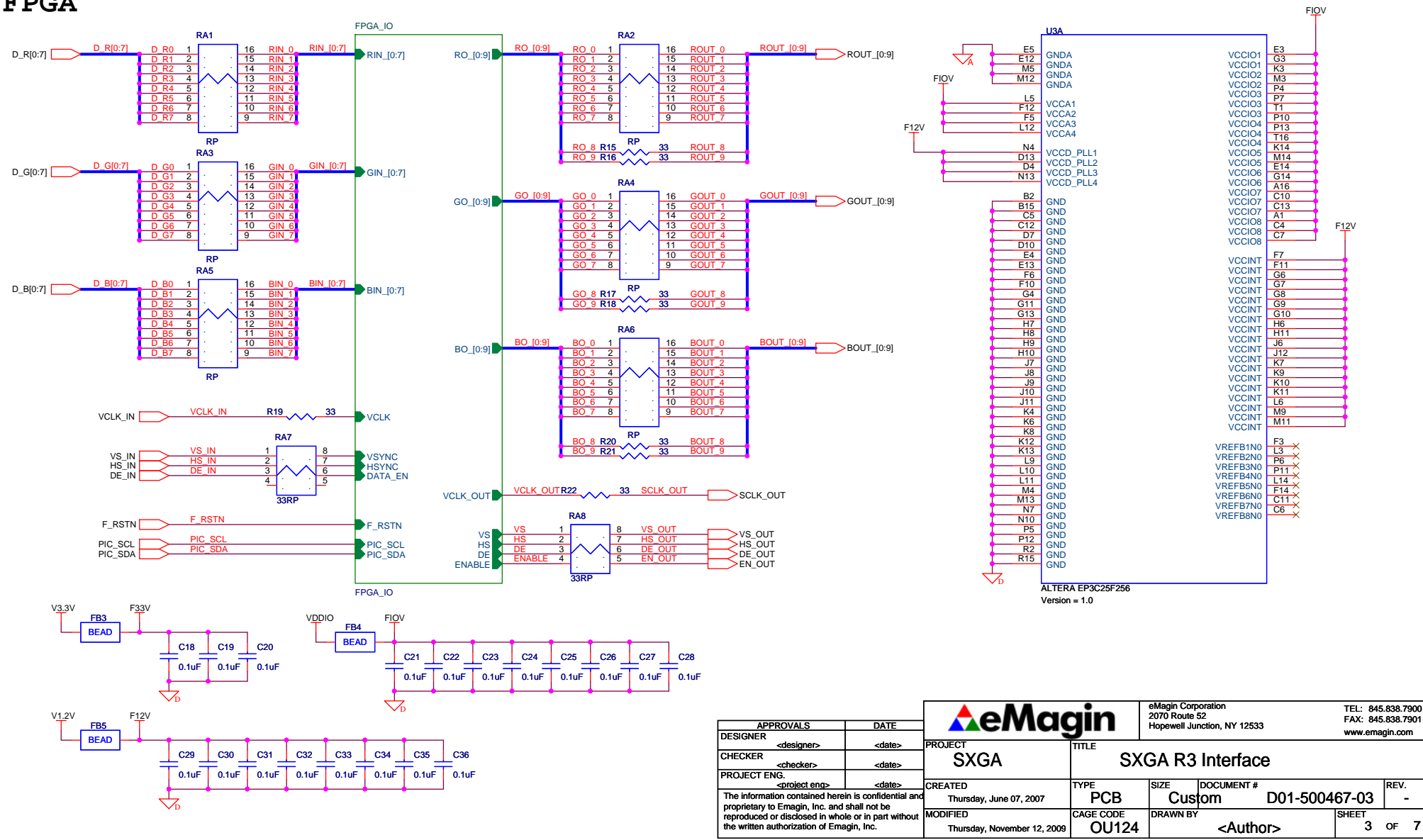
- Note 1. Page 3 MCU; Remove R32 (0 ohms) and connect U5 - 2 to VD2.5 at FB13
- Note 2. Page 3 MCU; Cut trace "VGN\_IN" at U5 - 27 and insert 10K resistor in series and 0.1uF to Ground.
- Note 3. Page 3 MCU; Connect U5 - 28 (TP15) to page 5 (FPGA\_IO) TP6 (VS)
- Note 4: Change board part number to C01-500467--03 in bottom copper layer
- Note: 5: Change Assembly number on top layer silkscreen to A01-500467-03
- Note6: Change Top silkscreen legend from SXGA Drive Board Rev2 to: SXGA Design Reference Board
- Note 7: Add on Top Silkscreen layer a white rectangle approximately 5mm hihg by 20 mm long, preceded by the mention S/N:

GBE BOARD V1.1

APPROVALS		DATE		 <div>eMagin Corporation 2070 Route 52 Hopewell Junction, NY 12533</div> <div>TEL: 845.838.7900 FAX: 845.838.7901 www.emagin.com</div>	PROJECT <b>SXGA</b>	TITLE <b>SXGA R3 Interface</b>							
DESIGNER	<designer>	<date>											
CHECKER	<checker>	<date>											
PROJECT ENG.	<project eng>	<date>											
The information contained herein is confidential and proprietary to Emagin, Inc. and shall not be reproduced or disclosed in whole or in part without the written authorization of Emagin, Inc.						CREATED Friday, August 08, 2003	TYPE <b>PCB</b>	SIZE <b>B</b>	DOCUMENT # <b>D01-500467-03</b>	REV. -			
						MODIFIED Thursday, November 12, 2009	CAGE CODE <b>OU124</b>	DRAWN BY	SHEET <b>1</b> OF <b>7</b>				



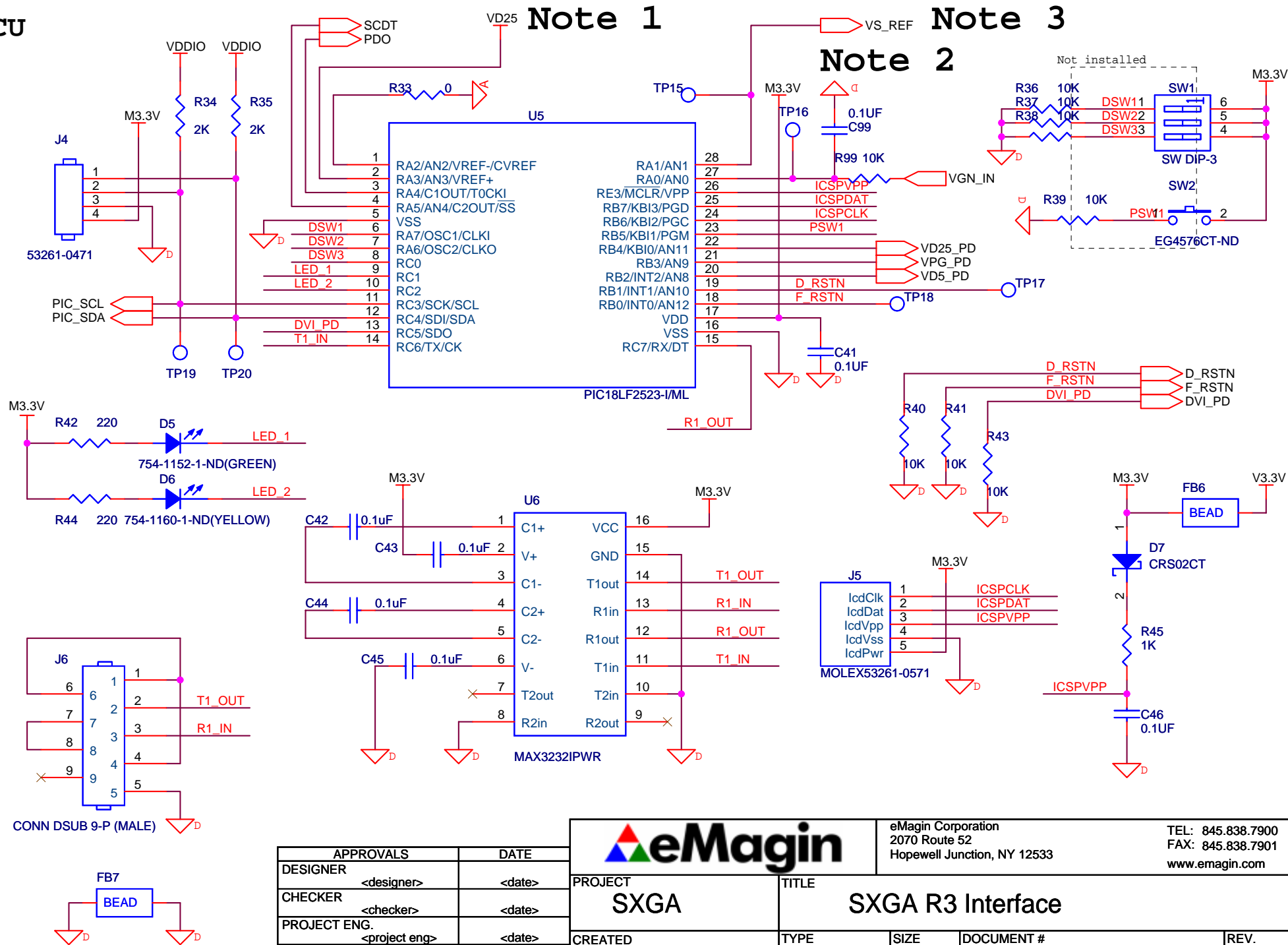
FPGA




APPROVALS		DATE		PROJECT		TITLE	
DESIGNER	<designer>	CHECKER	<checker>	PROJECT	SXGA	TITLE	SXGA R3 Interface
PROJECT ENG.	<project eng>	CREATED	Thursday, June 07, 2007	TYPE	PCB	SIZE	Custom
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				CAGE CODE	OU124	DRAWN BY	<Author>
				SHEET		3	OF 7



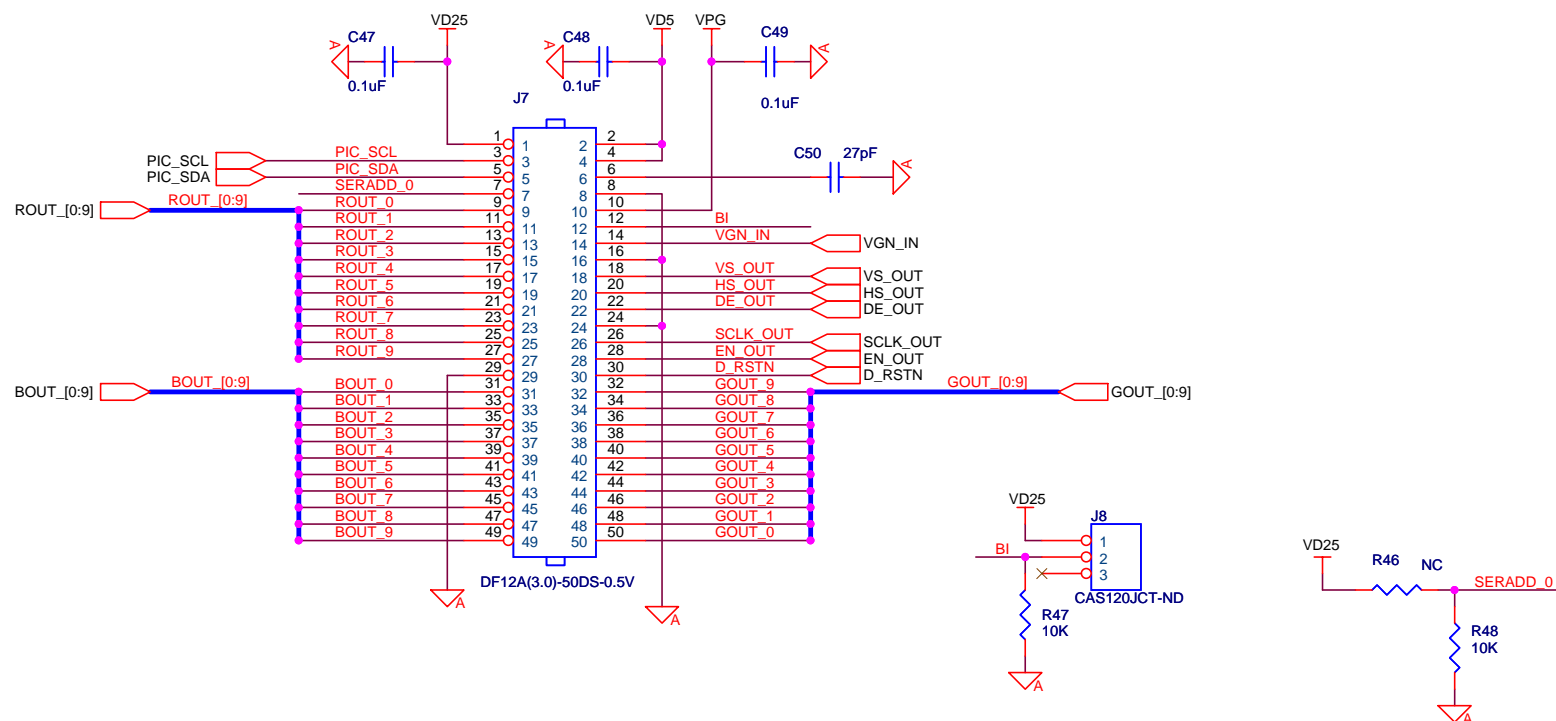
MCU



APPROVALS	DATE
DESIGNER	<designer>
CHECKER	<checker>
PROJECT ENG.	<project eng>
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PROJECT SXGA		TITLE SXGA R3 Interface			
CREATED Thursday, June 07, 2007	TYPE PCB	SIZE Custom	DOCUMENT # D01-500467-03		REV. -
MODIFIED Thursday, November 12, 2009	CAGE CODE OU124	DRAWN BY			SHEET 5 OF 7

# OUTPUT

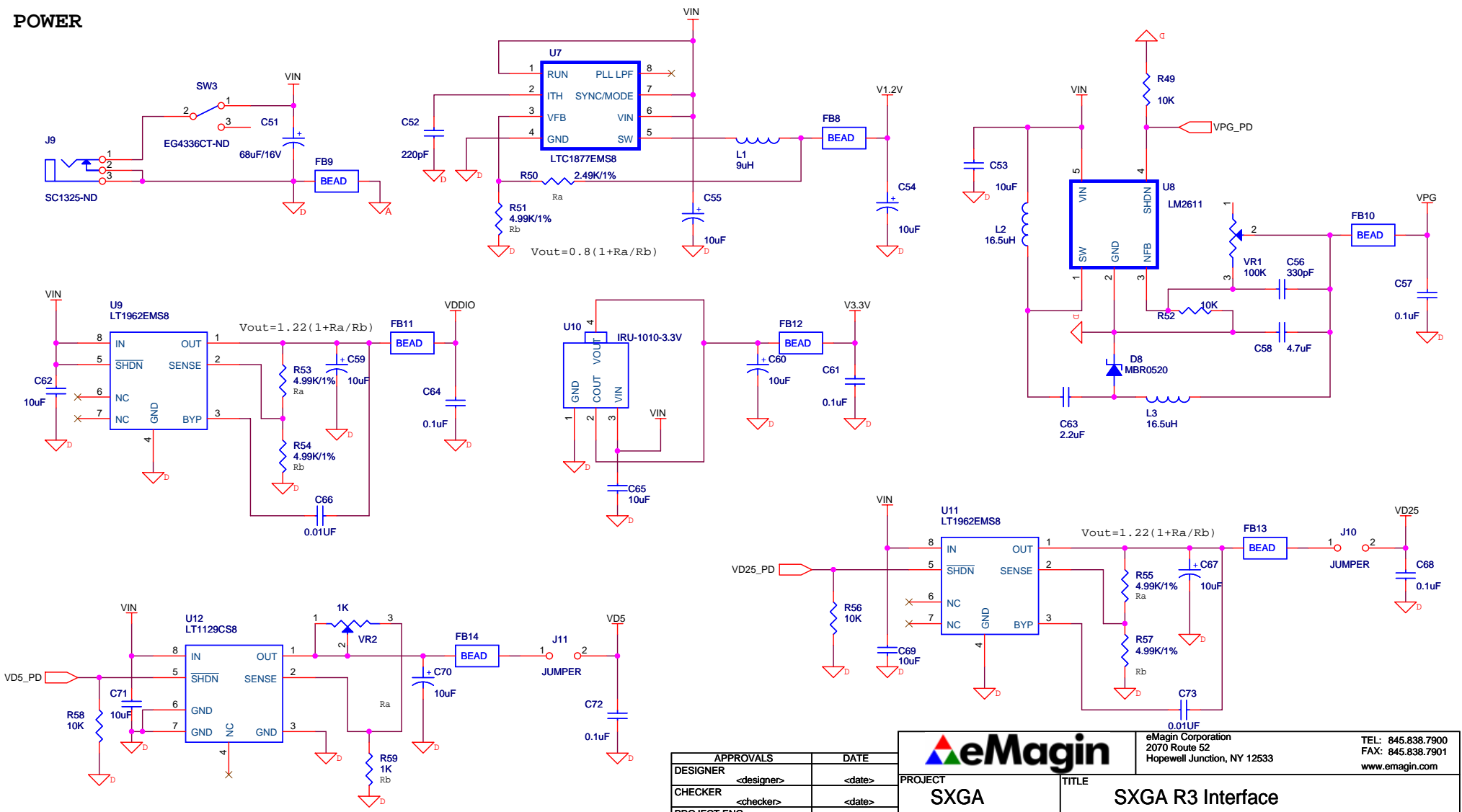



APPROVALS		DATE
DESIGNER	<designer>	<date>
CHECKER	<checker>	<date>
PROJECT ENG.	<project eng>	<date>

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PROJECT SXGA		TITLE SXGA R3 Interface			
CREATED Thursday, February 07, 2008		TYPE PCB	SIZE Custom	DOCUMENT # D01-500467-03	REV. -
MODIFIED Thursday, November 12, 2009		CAGE CODE OU124	DRAWN BY		SHEET 6 OF 7

POWER



APPROVALS		DATE			eMagin Corporation 2070 Route 52 Hopewell Junction, NY 12533		TEL: 845.838.7900 FAX: 845.838.7901 www.emagin.com	
DESIGNER	<designer>	<date>						
CHECKER	<checker>	<date>						
PROJECT ENG.	<project eng>	<date>						
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				SXGA	SXGA R3 Interface			
				CREATED	TYPE	SIZE	DOCUMENT #	REV.
				Friday, February 01, 2008	PCB	Custom	D01-500467-03	-
				MODIFIED	CAGE CODE	DRAWN BY		SHEET
				Thursday, November 12, 2009	OU124			7 OF 7